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Analysis of the Possibility of Implementing Synchronization Devices Operating on OFDM Technology on CMOS Transistors with Submicron Design Standards in High-Speed Data Transmission Systems

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Performance analysis of PLL components in digital synchronization systems for high-speed applications

Macarevici A., Kinash A., Tocari M., Chubarov V.

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2018. New Jersey, SUA. [ISBN 978-153866474-2](#).

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Integral gauge pressure sensor with frequency output signal

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